

Serial No. **09/916,318**

Docket No. **P-0217**

Amtdt. dated April 6, 2006

Reply to Office Action of January 13, 2006

REMARKS

By the present response, Applicant has amended claims 12, 15 and 32 to further clarify the invention. Claims 1-25 and 27-35 remain pending in the present application. Reconsideration and withdrawal of the outstanding rejections and allowance of the present application are respectfully requested in view of the above amendments and the following remarks.

In the Office Action, claims 12-17 have been rejected under 35 U.S.C. § 112, first paragraph. Claims 32 and 35 have been rejected under 35 U.S.C. § 103(a) as being unpatentable over U.S. Patent No. 6,640,100 (Kojima et al.) in view of U.S. Patent No. 6,014,546 (Georges et al.). Claims 1-11, 18-25, 27-31, 33 and 34 are allowed.

Allowable Subject Matter

Applicant thanks the Examiner for allowing claims 1-11, 18-25, 27-31, 33 and 34.

35 U.S.C. § 112 Rejections

Claims 12-17 have been rejected under 35 U.S.C. § 112, first paragraph. The Examiner asserts that the limitation “the WLL transmitting processor and WLAN receiving processor share a phase locked loop” was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor at the time the application was filed, had possession of the claimed invention. Applicant respectfully traverses these rejections and submits that these limitations are fully supported in Applicant’s specification and drawings.

Specifically, as disclosed in Applicant's specification on page 17, paragraph 46, the integrated WLL and WLAN transceiver apparatus allows five mixers to share the PLL 250 using a plurality of distributors. As shown in Figure 5, mixers 130 and 135, located in WLL receiving processor 4, share the PLL 250. Moreover, mixers 138 and 228 located in the WLAN transmitting processor 2 also share the PLL 250. Therefore, Applicants specification and drawings clearly support that the WLL receiving processor (processor 4 including mixers 130 and 135) and the WLAN transmitting processor (processor 2 including mixers 138 and 228) share the phase locked loop (PLL 250).

Accordingly, Applicants respectfully request that these rejections be withdrawn and that these claims be allowed.

35 U.S.C. § 103 Rejections

Claims 32 and 35 have been rejected under 35 U.S.C. § 103(a) as being unpatentable over Kojima et al. in view of Georges et al. Applicant respectfully traverses these rejections.

Regarding claim 32, Applicant submits that this claim has been amended with subject matter from claim 18, that has been allowed by the Examiner and, therefore, is patentable over the cited references at least for these reasons.

Regarding claim 35, Applicant submits that this claim is dependent on independent claim 18 that has been allowed by the Examiner and, therefore, is patentable at least for the same reasons noted regarding this independent claim.

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Accordingly, Applicant submits that none of the cited references taken alone or in any proper combination, disclose suggest or render obvious the limitations in the combination of each of claims 32 and 35 of the present application. Applicant respectfully requests that these rejections be withdrawn and that these claims be allowed.

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CONCLUSION

In view of the foregoing Amendments and remarks, Applicant submits that claims 1-25 and 27-35 are now in condition for allowance. Accordingly, early allowance of such claims is respectfully requested. If the Examiner believes that any additional changes would place the application in better condition for allowance, the Examiner is invited to contact the undersigned attorney, Frederick D. Bailey, at the telephone number listed below.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this, concurrent and future replies, including extension of time fees, to Deposit Account 16-0607 and please credit any excess fees to such deposit account.

Respectfully submitted,
FLESHNER & KIM, LLP



Daniel Y.J. Kim
Registration No. 36,186
Frederick D. Bailey
Registration No. 42,282

P.O. Box 221200
Chantilly, Virginia 20153-1200
(703) 766-3701 DYK/FDB:tlg

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Please direct all correspondence to Customer Number 34610